

# Fault Diagnoses and Tolerance in Cryptography 2022

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# Zoom Conference Notes

- Please use the **Q&A function** to ask questions.
- Please use the **chat function** for general conference chat.

The screenshot shows a Zoom conference slide with logos for **leti cea tech** and **Verimag**. The slide text includes "AN EN FAULT ATTA", "FDTC 20", "ULTI-IENT", and "3/09/2020". A "Question and Answer" window is overlaid on the slide, displaying a "Welcome" message: "Welcome. Feel free to ask the host and panelists questions." Below the message is a text input field containing "How many devices did you test with?". At the bottom of the window are three buttons: "Send anonymously" (with an unchecked checkbox), "Cancel", and "Send".

<sup>1</sup> Univ. Grenoble Alpes, CEA, LETI, DSYS, CESTI,  
<sup>2</sup> Univ. Grenoble Alpes, CNRS, VERIMAG, F-38000 Gr

# Papers

## Submitted Papers:

- 15 papers submitted
- Reviewed with PC members and additional reviewers
- Most papers received 3 reviews.

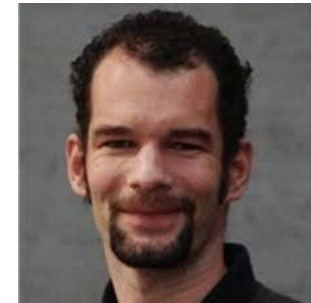
## Accepted Papers:

- 7 regular papers (around 54%)
- 1 short paper (around 25%)
- Overall, around 53% acceptance rate

The proceedings will be published by CPS (on IEEExplore).

# Invited Talks

- Statistical Ineffective Fault Attacks (SIFA)
  - Florian Mendel
  - Cryptographer & Security Architect, Infineon Technologies
- Pre-silicon fault simulation: Hard and Important
  - Jasper van Woudenberg
  - CTO North America, RISCURE



# Program Schedule

***Start: 10:00 CEST (04:00 am New York – 05:00 pm Tokyo)***

10:00 – 10:10      Welcome and Opening remarks

## ***Keynote I***

*Chair: Shivam Bhasin*

10:10 – 10:50      Statistical Ineffective Fault Attacks (SIFA)  
*Florian Mendel*

10:50 – 11:00      Break



# Program Schedule

## **Session 1 – Laser Fault Attacks**

*Chair: Luca Breveglieri*

- |               |   |
|---------------|---|
| 11:00 – 11:15 | Embedded-EEPROM descrambling via laser-based techniques – A case study on AVR MCU<br><i>Samuel Chef, Chung Tah Chua, Jing Yun Tay, Jason Jun Wei Cheah and Chee Lip Gan</i> |
| 11:15 – 11:30 | Triple Exploit Chain with Laser Fault Injection on a Secure Element<br><i>Olivier Hériveaux</i>   |
| 11:30 – 11:45 | The More You Know: Improving Laser Fault Injection with Prior Knowledge<br><i>Marina Krček, Thomas Ordas, Daniele Fronte and Stjepan Picek</i>                              |
| 11:45 – 12:00 | Break   |

## **Session 2 – Fault Attacks to Public Key Cryptosystems**

*Chair: Luca Breveglieri*

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|---------------|---|
| 12:00 – 12:15 | FA-LLing for RSA: Lattice-based Fault Attacks against RSA Encryption and Signature<br><i>Guillaume Barbu</i>  |
| 12:15 – 12:30 | Generalising Fault Attacks to Genus Two Isogeny Cryptosystems<br><i>Ariana Goh, Chu-Wee Lim and Yan Bo Ti</i> |
| 12:30 – 13:15 | Break   |

# Program Schedule

## ***Session 3 – Fault Injection: Techniques, Analysis, Effects***

*Chair: Luca Breveglieri*

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|---------------|--|
| 13:15 – 13:30 | Body Biasing Injection: Impact of substrate types on the induced disturbances?<br><i>Geoffrey Chancel, Jean-Marc Gallière and Philippe Maurine</i>                 |
| 13:30 – 13:45 | Quantifying the Speed-Up Offered by Genetic Algorithms during Fault Injection Cartographies<br><i>Idris Rais-Ali, Antoine Bouvet and Sylvain Guilley</i>           |
| 13:45 – 14:00 | Exploration of Fault Effects on Formal RISC-V Microarchitecture Models<br><i>Simon Tollec, Mihail Asavoaie, Damien Couroussé, Karine Heydemann and Mathieu Jan</i> |
| 14:00 – 14:10 | Break  |

## ***Keynote II***

*Chair: Luca Breveglieri*

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|---------------|--|
| 14:10 – 14:50 | Pre-silicon fault simulation: hard and important<br><i>Jasper van Woudenberg</i> |
| 14:50 – 15:00 | Closing remarks and Farewell   |

Congratulations to all the Authors  
Wishing you a wonderful FDTC 2022!